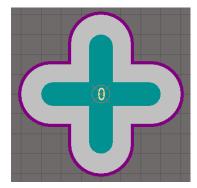
Footprint Creating Q & A

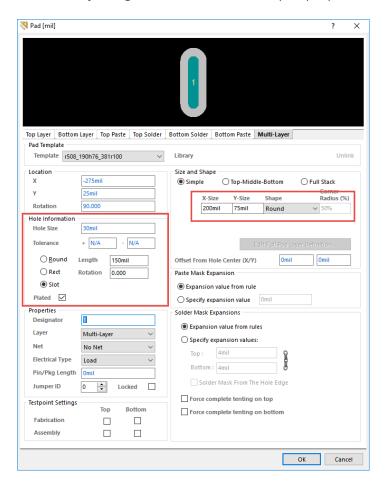
Question / Comment	Answer/Reply
Is Class A/B/C the same as Class 1/2/3? Is	We stand corrected - it is Class 1, 2, and 3, where:
it typical to specify this class on the fab	
drawing to the turnkey house? We don't	Class 1 – General Electronic Products
typically communicate directly with the	Class 2 – Dedicated Services Electronic Products
fab house	Class 3 – High Reliability Electronic Products
	There are also Levels, as well which are defined as:
	Level A – General Design Complexity
	Level B – Moderate Design Complexity
	Level C – High Design Complexity
	Unfortunately, we didn't have time to touch on these.
	They are important, as well.
	Levels and Classes (along with Types) need to be
	discussed with the fabricator since they dictate the level
	of complexity, tolerances, and the pass/fail criteria for
	visual inspection. We also encourage you to seek the
	CID certification from the IPC. They go into more detail
	on this important subject.
The datasheet shows the bottom view of	We stand corrected, though it would be nice if TI
the device. The footprint needs to be	specified the view angle in the datasheet.
constructed from the top view. The pin	
layout designed in the demo will not	This brings up several good points:
work. Needs to be mirrored.	1. This is substant and a sudd by assistant by
The pin designators are reversed the	 This is why components should be reviewed by our peers.
drawing is from the bottom and the PCB	2. If one was to obtain a STEP model with the tab
pattern is from the top	orientation, the STEP model upon placement
	would have shown that the footprint and tab
	were mirrored.
	3. When in doubt about the datasheet, contact the
	manufacturer.
Are the pad via library functions available	The Pad Via library was introduced in Altium Designer
for Altium 14?	version 15.

What is the industry best practice for holes that do not require electrical connection (i.e., mounting features on connectors, holes for mounting the PCB without requiring electrical connection)? I've seen designs where these holes are NPTH (Non-Plated Through holes) and I've seen designs where they are PTH. I had heard once that NPTH are more difficult to manufacture. Not sure if that's true. I prefer not to have floating copper	Plating is primarily used for electrical and thermal connectivity, and it usually serves both the purpose of grounding the component and providing a path for thermal dissipation. If one is going to plate the hole, it is best that the copper be tied to ground. Otherwise, there is the possibly that it could become an accidental antenna. As for cost, it is easier to plate holes than to mask them since the board is plated "en masse" and not individually.
on the board. I'm assuming there is NOT a standard for mechanical layer usage, but if there is something recommended where would I find that information	You are correct – there is no standard for mechanical layers. It you are looking for a comprehensive list, check out this posting on Altium's forums. https://forum.live.altium.com/posts/75191 If you are going to search on forms, it is recommended that you type +Mechanical +Layers +Darren +Moore. Darren was instrumental in leading a number of forums posts on this topic.
Where is the "convert special string" option?	There are far more layers than what you will need. Use layers that make sense to you and your organization. Altium has available special strings in both the schematic and PCB editors. In the schematic editor, they start with an equal sign (=); in the PCB editor, they start with a period (.) By placing these special strings, Altium can provide information such as date, time, designator, etc. When one uses these special strings, the name of the special string is visible. To convert the special string to its value in the PCB editor:
For the odd shaped pad that is made using a polygon, Will the entire polygon be plated when the PCB is manufactured?	View Configurations (shortcut "L" key), View Options tab. In the region titled "Display Options", check the box for "Convert Special Strings." Regardless of the copper primitives used, as long as there is copper on the board, it will be present in the fabrication files (Gerbers, ODB++, IPC-2581). However, a word of caution when it comes to the solder mask and solder paste. One must enable these features if an odd shaped polygon pours (which Altium will convert to a copper region) is being used in a footprint.

What about odd shaped through hole pads? For example, cross shaped through hole pad You can do a slot in Altium Designer:



This is achieved by using the slot features in the pad properties:



In this example, 2 pads were used with the same designator; one was turned 90 degrees.